

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	50	(US-5717895-\$ or US-5608892-\$ or US-5875466-\$ or US-5511212-\$ or US-5430861-\$ or US-5664149-\$ or US-5255383-\$ or US-5479630-\$ or US-5524234-\$ or US-5535116-\$ or US-5659708-\$ or US-5689680-\$ or US-5860111-\$ or US-5991853-\$ or US-4953121-\$ or US-5274782-\$ or US-5940864-\$ or US-5996049-\$ or US-6018791-\$ or US-6199147-\$ or US-4456952-\$ or US-4899275-\$ or US-4953073-\$ or US-5321698-\$ or US-5345578-\$ or US-5561780-\$).did. or (US-5642486-\$ or US-5692151-\$ or US-5724549-\$ or US-5729712-\$ or US-5732278-\$ or US-5732409-\$ or US-5778433-\$ or US-5826053-\$ or US-5835955-\$ or US-5857183-\$ or US-5875470-\$ or US-5900023-\$ or US-5933856-\$ or US-5940856-\$ or US-5943685-\$ or US-5943684-\$ or US-5946709-\$ or US-5956743-\$ or US-5963974-\$ or US-6070166-\$ or US-6108663-\$ or US-6128639-\$ or US-6148368-\$ or US-6173370-\$). did.	USPAT	OR	ON	2006/09/13 19:26
L2	723	((bypass or bypassed or bypassing) near5 (cache or cached or caching)) with data)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:55
L3	1	1 and 2	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:27
L4	1399	(bypass or bypassed or bypassing) near5 (cache or cached or caching)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:29
L5	5	1 and 4	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:29
L6	4	5 not 3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:31
L7	1968	711/118.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:31
L8	335	711/138.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:31
L9	1116	711/144.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:31

EAST Search History

L10	832	711/145.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:31
L11	2314	711/154.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:31
L12	5642	7 or 8 or 9 or 10 or 11	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:32
L13	190	2 and 12	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:32
L14	0	"NOT" near3 (cache or cached or caching)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:33
L15	0	"NOT" with (cache or cached or caching)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:42
L16	138982	(status adj bit) or ((update or updated) with data)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:43
L17	108	13 and 16	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:43
L18	39	8 and 17	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:43
L19	14216068	@ad<"20040319"	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:44
L20	38	18 and 19	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:44
L21	8	(US-20040117441-\$ or US-20010052053-\$).did. or (US-6922754-\$ or US-6356980-\$ or US-6131155-\$ or US-5918069-\$ or US-5809550-\$ or US-5729713-\$).did.	US-PGPUB; USPAT	OR	ON	2006/09/13 19:59
L22	22	((((bypass or bypassed or bypassing) near5 (cache or cached or caching)) with threshold)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:56

EAST Search History

L23	17	19 and 22	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:56
L24	3	17 and 23	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:56
L25	3767	((threshold near5 (amount or quantity)) with data)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 20:00
L26	0	24 and 25	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 20:00
L27	1	21 and 25	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 20:00
L28	1	4 and 27	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 20:00
L29	531	711/118.ccls.	US-PGPUB	OR	ON	2006/09/13 20:01
L30	375	19 and 29	US-PGPUB	OR	ON	2006/09/13 20:01
L31	0	4 and 25 and 30	US-PGPUB	OR	ON	2006/09/13 20:02
L32	8	4 and 16 and 30	US-PGPUB	OR	ON	2006/09/13 20:02
L33	0	25 and 32	US-PGPUB	OR	ON	2006/09/13 20:02

☐ Search Session History

[BROWSE](#)

[SEARCH](#)

[IEEE XPLORE GUIDE](#)

[SUPPORT](#)

Edit an existing query or compose a new query in the Search Query Display.

Wed, 13 Sep 2006, 8:17:06 PM EST

Search Query Display

Select a search number (#) to:

- Add a query to the Search Query Display
- Combine search queries using AND, OR, or NOT
- Delete a search
- Run a search

Recent Search Queries

- | | |
|----|--|
| #1 | ((cache <and> bypass)<in>metadata) |
| #2 | ((cache <and> bypass)<in>metadata) |
| #3 | ((cache <and> bypass)<in>metadata) |
| #4 | (((((cache <and> bypass)<in>metadata))<AND>(threshold <or> thresholds<in>metadata))) |
| #5 | (((((cache <and> bypass)<in>metadata))<AND>(threshold <or> thresholds<in>metadata))) |

Results for "(((cache <and> bypass)<in>metadata))<and>(threshold <or> thresholds<in>g..."

Your search matched 17 of 56 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order.

e-mail printer

» Search Options

[View Session History](#)

[New Search](#)

Modify Search

(((cache <and> bypass)<in>metadata))<and>(threshold <or> thresholds<in>metadat

Check to search only within this results set








Display Format: ☒ Citation ☐ Citation & Abstract

view selected items Select All Deselect All

» Key

IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

- ☐ **1. Compiler managed micro-cache bypassing for high performance EPIC processors**
 Youfeng Wu; Rakvic, R.; Li-Ling Chen; Chyi-Chang Miao; Chrysos, G.; Fang, J.;
[Microarchitecture, 2002. \(MICRO-35\). Proceedings. 35th Annual IEEE/ACM International Symposium on](#)
 18-22 Nov. 2002 Page(s):134 - 145
 Digital Object Identifier 10.1109/MICRO.2002.1176245
[AbstractPlus](#) | Full Text: [PDF\(502 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ **2. DHTTP: an efficient and cache-friendly transfer protocol for Web traffic**
 Rabinovich, M.; Hua Wang;
[INFOCOM 2001. Twentieth Annual Joint Conference of the IEEE Computer and Communications Societies.](#)
[Proceedings. IEEE](#)
 Volume 3, 22-26 April 2001 Page(s):1597 - 1606 vol.3
 Digital Object Identifier 10.1109/INFCOM.2001.916656
[AbstractPlus](#) | Full Text: [PDF\(152 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ **3. Discretionary caching for I/O on clusters**
 Vilayannur, M.; Sivasubramaniam, A.; Kandemir, M.; Thakur, R.; Ross, R.;
 Cluster Computing and the Grid, 2003. Proceedings. CCGrid 2003. 3rd IEEE/ACM International Symposium on
 12-15 May 2003 Page(s):96 - 103
 Digital Object Identifier 10.1109/CCGRID.2003.1199357
[AbstractPlus](#) | Full Text: [PDF\(302 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ **4. Optically assisted Internet routing using arrays of novel dynamically reconfigurable FBG-based correla:**
 Hauer, M.C.; McGeehan, J.E.; Kumar, S.; Touch, J.D.; Bannister, J.; Lyons, E.R.; Lin, C.H.; Au, A.A.; Lee, H.P.
 Starodubov, D.S.; Willner, A.E.;
[Lightwave Technology. Journal of](#)
 Volume 21, Issue 11, Nov. 2003 Page(s):2765 - 2778
 Digital Object Identifier 10.1109/JLT.2003.819144
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(829 KB\)](#) IEEE JNL
[Rights and Permissions](#)
- ☐ **5. Efficient Error Recovery for Multimedia Data Transmission over 3G Cellular Broadcast Networks**
 Joonho Lee; Kyungtae Kang; Yongwoo Cho; Junu Kim; Heonshik Shin;
[Advanced Information Networking and Applications, 2006. AINA 2006. 20th International Conference on](#)
 Volume 1, 18-20 April 2006 Page(s):683 - 688
 Digital Object Identifier 10.1109/AINA.2006.163
[AbstractPlus](#) | Full Text: [PDF\(296 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ **6. Predictive techniques for aggressive load speculation**

- Reinman, G.; Calder, B.;
[Microarchitecture, 1998. MICRO-31. Proceedings. 31st Annual ACM/IEEE International Symposium on](#)
30 Nov.-2 Dec. 1998 Page(s):127 - 137
Digital Object Identifier 10.1109/MICRO.1998.742775
[AbstractPlus](#) | Full Text: [PDF\(152 KB\)](#) IEEE CNF
[Rights and Permissions](#)
-  7. **Energy-aware code cache management for memory-constrained Java devices**
Chen, G.; Kandemir, M.; Vijaykrishnan, N.; Irwin, M.J.;
[SOC Conference, 2003. Proceedings. IEEE International \(Systems-on-Chip\)](#)
17-20 Sept. 2003 Page(s):179 - 182
Digital Object Identifier 10.1109/SOC.2003.1241488
[AbstractPlus](#) | Full Text: [PDF\(400 KB\)](#) IEEE CNF
[Rights and Permissions](#)
-  8. **Proactive Reed-Solomon bypass (PRSB): a technique for real-time multimedia processing in 3G cellular broadcast networks**
Yongwoo Cho; Kyungtae Kang; Joonho Lee; Heonshik Shin;
[Embedded and Real-Time Computing Systems and Applications, 2005. Proceedings. 11th IEEE International C](#)
[on](#)
17-19 Aug. 2005 Page(s):532 - 538
Digital Object Identifier 10.1109/RTCSA.2005.71
[AbstractPlus](#) | Full Text: [PDF\(176 KB\)](#) IEEE CNF
[Rights and Permissions](#)
-  9. **A runtime auto scalable power-efficient instruction-cache design**
Tay Teng Tiow; Zhu Xiaoping;
[Circuits and Systems, 2005. ISCAS 2005. IEEE International Symposium on](#)
23-26 May 2005 Page(s):5270 - 5273 Vol. 5
Digital Object Identifier 10.1109/ISCAS.2005.1465824
[AbstractPlus](#) | Full Text: [PDF\(224 KB\)](#) IEEE CNF
[Rights and Permissions](#)
-  10. **Use-based register caching with decoupled indexing**
Butts, J.A.; Sohi, G.S.;
[Computer Architecture, 2004. Proceedings. 31st Annual International Symposium on](#)
19-23 June 2004 Page(s):302 - 313
Digital Object Identifier 10.1109/ISCA.2004.1310783
[AbstractPlus](#) | Full Text: [PDF\(327 KB\)](#) IEEE CNF
[Rights and Permissions](#)
-  11. **Using service brokers for accessing backend servers for web applications**
Huamin Chen; Mohapatra, P.;
[Distributed Computing Systems Workshops, 2003. Proceedings. 23rd International Conference on](#)
19-22 May 2003 Page(s):928 - 933
Digital Object Identifier 10.1109/ICDCSW.2003.1203670
[AbstractPlus](#) | Full Text: [PDF\(266 KB\)](#) IEEE CNF
[Rights and Permissions](#)
-  12. **Interoperation of copy avoidance in network and file I/O**
Brustoloni, J.C.;
[INFOCOM '99. Eighteenth Annual Joint Conference of the IEEE Computer and Communications Societies. Pro](#)
[IEEE](#)
Volume 2, 21-25 March 1999 Page(s):534 - 542 vol.2
Digital Object Identifier 10.1109/INFCOM.1999.751387
[AbstractPlus](#) | Full Text: [PDF\(852 KB\)](#) IEEE CNF
[Rights and Permissions](#)
-  13. **In-system FPGA prototyping of an Itanium microarchitecture**
Wunderlich, R.E.; Hoe, J.C.;
[Computer Design: VLSI in Computers and Processors, 2004. ICCD 2004. Proceedings. IEEE International Con](#)
[on](#)
11-13 Oct. 2004 Page(s):288 - 294
Digital Object Identifier 10.1109/ICCD.2004.1347935



14. Using cache mapping to improve memory performance handheld devices

Rong Xu; Zhiyuan Li;

[Performance Analysis of Systems and Software, 2004 IEEE International Symposium on - ISPASS 2004](#) Page(s):106 - 114

Digital Object Identifier 10.1109/ISPASS.2004.1291362

[AbstractPlus](#) | [Full Text: PDF\(1733 KB\)](#) IEEE CNF
[Rights and Permissions](#)



15. The latency hiding effectiveness of decoupled access/execute processors

Parcerisa, J.-M.; Gonzalez, A.;

[Euromicro Conference, 1998. Proceedings. 24th](#)

Volume 1, 25-27 Aug. 1998 Page(s):293 - 300 vol.1

Digital Object Identifier 10.1109/EURMIC.1998.711813

[AbstractPlus](#) | [Full Text: PDF\(764 KB\)](#) IEEE CNF
[Rights and Permissions](#)



16. Putting the fill unit to work: dynamic optimizations for trace cache microprocessors

Friendly, D.H.; Patel, S.J.; Patt, Y.N.;

[Microarchitecture, 1998. MICRO-31. Proceedings. 31st Annual ACM/IEEE International Symposium on](#) 30 Nov.-2 Dec. 1998 Page(s):173 - 181

Digital Object Identifier 10.1109/MICRO.1998.742779

[AbstractPlus](#) | [Full Text: PDF\(96 KB\)](#) IEEE CNF
[Rights and Permissions](#)



17. Compiler-directed early load-address generation

Ben Chung Cheng; Connors, D.A.; Hwu, W.W.;

[Microarchitecture, 1998. MICRO-31. Proceedings. 31st Annual ACM/IEEE International Symposium on](#) 30 Nov.-2 Dec. 1998 Page(s):138 - 147

Digital Object Identifier 10.1109/MICRO.1998.742776

[AbstractPlus](#) | [Full Text: PDF\(240 KB\)](#) IEEE CNF
[Rights and Permissions](#)

☐ Search Results

BROWSE

SEARCH

IEEE XPLORE GUIDE

SUPPORT

Results for "((cache <and> bypass)<in>metadata)"

Your search matched 56 of 1408155 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order.

e-mail printer

» Search Options

[View Session History](#)

[New Search](#)

Modify Search

((cache <and> bypass)<in>metadata)

Search

☐ Check to search only within this results set

Display Format: ☒ Citation ☐ Citation & Abstract

» Key

IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

view selected items

[Select All](#) [Deselect All](#)

View: 1-25 | 26-

- ☐ 1. **Run-time cache bypassing**
Johnson, T.L.; Connors, D.A.; Merten, M.C.; Hwu, W.-M.W.;
[Computers, IEEE Transactions on](#)
Volume 48, Issue 12, Dec. 1999 Page(s):1338 - 1354
Digital Object Identifier 10.1109/12.817393
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(1208 KB) IEEE JNL
[Rights and Permissions](#)
- ☐ 2. **Run-time adaptive cache management**
Johnson, T.L.; Connors, D.A.; Hwu, W.W.;
[System Sciences, 1998., Proceedings of the Thirty-First Hawaii International Conference on](#)
Volume 7, 6-9 Jan. 1998 Page(s):774 - 775 vol.7
Digital Object Identifier 10.1109/HICSS.1998.649283
[AbstractPlus](#) | Full Text: [PDF](#)(96 KB) IEEE CNF
[Rights and Permissions](#)
- ☐ 3. **Performance comparison of alternative Web caching techniques**
Hassanein, H.; Zhengang Liang; Martin, P.;
[Computers and Communications, 2002. Proceedings. ISCC 2002. Seventh International Symposium on](#)
1-4 July 2002 Page(s):213 - 218
Digital Object Identifier 10.1109/ISCC.2002.1021681
[AbstractPlus](#) | Full Text: [PDF](#)(260 KB) IEEE CNF
[Rights and Permissions](#)
- ☐ 4. **Use-based register caching with decoupled indexing**
Butts, J.A.; Sohi, G.S.;
[Computer Architecture, 2004. Proceedings. 31st Annual International Symposium on](#)
19-23 June 2004 Page(s):302 - 313
Digital Object Identifier 10.1109/ISCA.2004.1310783
[AbstractPlus](#) | Full Text: [PDF](#)(327 KB) IEEE CNF
[Rights and Permissions](#)
- ☐ 5. **Using cache mapping to improve memory performance handheld devices**
Rong Xu; Zhiyuan Li;
[Performance Analysis of Systems and Software, 2004 IEEE International Symposium on - ISPASS](#)
2004 Page(s):106 - 114
Digital Object Identifier 10.1109/ISPASS.2004.1291362
[AbstractPlus](#) | Full Text: [PDF](#)(1733 KB) IEEE CNF
[Rights and Permissions](#)
- ☐ 6. **Smart cache: an energy-efficient D-cache for a software MPEG-2 video decoder**
Chia-Lin Yang; Hung-Wei Tseng; Chia-Chiang Ho;
[Information, Communications and Signal Processing, 2003 and the Fourth Pacific Rim Conference on Multimec](#)

[AbstractPlus](#) | Full Text: [PDF](#)(433 KB) IEEE CNF

[Rights and Permissions](#)



7. Cache memories for data flow machines

Takesue, M.;

[Computers, IEEE Transactions on](#)

Volume 41, Issue 6, June 1992 Page(s):677 - 687

Digital Object Identifier 10.1109/12.144620

[AbstractPlus](#) | Full Text: [PDF](#)(952 KB) IEEE JNL

[Rights and Permissions](#)



8. Transparent distributed Web caching

Liang, Z.; Hassanein, H.; Martin, P.;

[Local Computer Networks, 2001. Proceedings. LCN 2001. 26th Annual IEEE Conference on](#)
14-16 Nov. 2001 Page(s):225 - 233

Digital Object Identifier 10.1109/LCN.2001.990791

[AbstractPlus](#) | Full Text: [PDF](#)(710 KB) IEEE CNF

[Rights and Permissions](#)



9. Improving cache performance by selective cache bypass

Chi, C.-H.; Dietz, H.;

[System Sciences, 1989. Vol.1: Architecture Track, Proceedings of the Twenty-Second Annual Hawaii International Conference on](#)

Volume 1, 3-6 Jan. 1989 Page(s):277 - 285 vol.1

Digital Object Identifier 10.1109/HICSS.1989.47168

[AbstractPlus](#) | Full Text: [PDF](#)(568 KB) IEEE CNF

[Rights and Permissions](#)



10. Bypass caching: making scientific databases good network citizens

Malik, T.; Burns, R.; Amitabh Chaudhary;

[Data Engineering, 2005. ICDE 2005. Proceedings. 21st International Conference on](#)
5-8 April 2005 Page(s):94 - 105

Digital Object Identifier 10.1109/ICDE.2005.30

[AbstractPlus](#) | Full Text: [PDF](#)(424 KB) IEEE CNF

[Rights and Permissions](#)



11. Scalable high-level caching for parallel I/O

Coloma, K.; Choudhary, A.; Liao, W.; Ward, L.; Russell, E.; Pundit, N.;

[Parallel and Distributed Processing Symposium, 2004. Proceedings. 18th International](#)
26-30 April 2004 Page(s):96

Digital Object Identifier 10.1109/IPDPS.2004.1303043

[AbstractPlus](#) | Full Text: [PDF](#)(1432 KB) IEEE CNF

[Rights and Permissions](#)



12. Compiler managed micro-cache bypassing for high performance EPIC processors

Youfeng Wu; Rakvic, R.; Li-Ling Chen; Chyi-Chang Miao; Chrysos, G.; Fang, J.;

[Microarchitecture, 2002. \(MICRO-35\). Proceedings. 35th Annual IEEE/ACM International Symposium on](#)
18-22 Nov. 2002 Page(s):134 - 145

Digital Object Identifier 10.1109/MICRO.2002.1176245

[AbstractPlus](#) | Full Text: [PDF](#)(502 KB) IEEE CNF

[Rights and Permissions](#)



13. A runtime auto scalable power-efficient instruction-cache design

Tay Teng Tiow; Zhu Xiaoping;

[Circuits and Systems, 2005. ISCAS 2005. IEEE International Symposium on](#)
23-26 May 2005 Page(s):5270 - 5273 Vol. 5

Digital Object Identifier 10.1109/ISCAS.2005.1465824

[AbstractPlus](#) | Full Text: [PDF](#)(224 KB) IEEE CNF

[Rights and Permissions](#)



14. Software assistance for data caches

Temam, O.; Drach, N.;
[High-Performance Computer Architecture, 1995. Proceedings. First IEEE Symposium on](#)
22-25 Jan. 1995 Page(s):154 - 163
Digital Object Identifier 10.1109/HPCA.1995.386546
[AbstractPlus](#) | Full Text: [PDF\(664 KB\)](#) IEEE CNF
[Rights and Permissions](#)



15. Discretionary caching for I/O on clusters

Vilayannur, M.; Sivasubramaniam, A.; Kandemir, M.; Thakur, R.; Ross, R.;
[Cluster Computing and the Grid, 2003. Proceedings. CCGrid 2003. 3rd IEEE/ACM International Symposium on](#)
12-15 May 2003 Page(s):96 - 103
Digital Object Identifier 10.1109/CCGRID.2003.1199357
[AbstractPlus](#) | Full Text: [PDF\(302 KB\)](#) IEEE CNF
[Rights and Permissions](#)



16. Reducing energy and delay using efficient victim caches

Memik, G.; Reinman, G.; Mangione-Smith, W.H.;
[Low Power Electronics and Design, 2003. ISLPED '03. Proceedings of the 2003 International Symposium on](#)
25-27 Aug. 2003 Page(s):262 - 265
Digital Object Identifier 10.1109/LPE.2003.1231873
[AbstractPlus](#) | Full Text: [PDF\(548 KB\)](#) IEEE CNF
[Rights and Permissions](#)



17. A method to improve the estimated worst-case performance of data caching

Lundqvist, T.; Stenstrom, P.;
[Real-Time Computing Systems and Applications, 1999. RTCSA '99. Sixth International Conference on](#)
13-15 Dec. 1999 Page(s):255 - 262
Digital Object Identifier 10.1109/RTCSA.1999.811244
[AbstractPlus](#) | Full Text: [PDF\(664 KB\)](#) IEEE CNF
[Rights and Permissions](#)



18. Improving the data cache performance of multiprocessor operating systems

Chun Xia; Torrellas, J.;
[High-Performance Computer Architecture, 1996. Proceedings. Second International Symposium on](#)
3-7 Feb. 1996 Page(s):85 - 94
Digital Object Identifier 10.1109/HPCA.1996.501176
[AbstractPlus](#) | Full Text: [PDF\(1084 KB\)](#) IEEE CNF
[Rights and Permissions](#)



19. A 2.25 gbytes/s 1 Mbit smart cache SRAM

Ku, J.; Siu, S.; Yazdani, M.; Yolin Lih; Wei-Ping Lu; Desroches, A.;
[VLSI Circuits, 1995. Digest of Technical Papers., 1995 Symposium on](#)
8-10 June 1995 Page(s):17 - 18
Digital Object Identifier 10.1109/VLSIC.1995.520667
[AbstractPlus](#) | Full Text: [PDF\(200 KB\)](#) IEEE CNF
[Rights and Permissions](#)



20. Reducing the latency of division operations with partial caching

Benowitz, E.; Ercegovic, M.; Fallah, F.;
[Signals, Systems and Computers, 2002. Conference Record of the Thirty-Sixth Asilomar Conference on](#)
Volume 2, 3-6 Nov. 2002 Page(s):1598 - 1602 vol.2
[AbstractPlus](#) | Full Text: [PDF\(487 KB\)](#) IEEE CNF
[Rights and Permissions](#)



21. DHTTP: an efficient and cache-friendly transfer protocol for Web traffic

Rabinovich, M.; Hua Wang;
[INFOCOM 2001. Twentieth Annual Joint Conference of the IEEE Computer and Communications Societies. Proceedings. IEEE](#)
Volume 3, 22-26 April 2001 Page(s):1597 - 1606 vol.3
Digital Object Identifier 10.1109/INFCOM.2001.916656
[AbstractPlus](#) | Full Text: [PDF\(152 KB\)](#) IEEE CNF
[Rights and Permissions](#)



22. Caching processor general registers

Yung, R.; Wilhelm, N.C.;

[Computer Design: VLSI in Computers and Processors, 1995. ICCD '95. Proceedings., 1995 IEEE International Conference on](#)

2-4 Oct. 1995 Page(s):307 - 312

Digital Object Identifier 10.1109/ICCD.1995.528826

[AbstractPlus](#) | Full Text: [PDF](#)(616 KB) IEEE CNF

[Rights and Permissions](#)



23. A server performance model for static Web workloads

Kant, K.; Sundaram, C.R.M.;

[Performance Analysis of Systems and Software, 2000. ISPASS, 2000 IEEE International Symposium on](#)
24-25 April 2000 Page(s):201 - 206

Digital Object Identifier 10.1109/ISPASS.2000.842301

[AbstractPlus](#) | Full Text: [PDF](#)(272 KB) IEEE CNF

[Rights and Permissions](#)



24. Energy-efficient register access

Tseng, J.H.; Asanovic, K.;

[Integrated Circuits and Systems Design, 2000. Proceedings. 13th Symposium on](#)
18-24 Sept. 2000 Page(s):377 - 382

Digital Object Identifier 10.1109/SBCCI.2000.876058

[AbstractPlus](#) | Full Text: [PDF](#)(468 KB) IEEE CNF

[Rights and Permissions](#)



25. Proactive Reed-Solomon bypass (PRSB): a technique for real-time multimedia processing in 3G cellular broadcast networks

Yongwoo Cho; Kyungtae Kang; Joonho Lee; Heonshik Shin;

[Embedded and Real-Time Computing Systems and Applications, 2005. Proceedings. 11th IEEE International C](#)
on

17-19 Aug. 2005 Page(s):532 - 538

Digital Object Identifier 10.1109/RTCSA.2005.71

[AbstractPlus](#) | Full Text: [PDF](#)(176 KB) IEEE CNF

[Rights and Permissions](#)

View: 1-25 | 26-

Indexed by
 Inspec

[Help](#) [Contact Us](#) [Privacy & Security](#)

© Copyright 2006 IEEE – All Rights Reserved